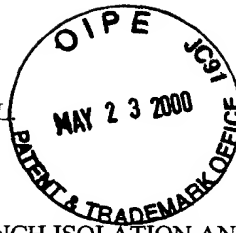


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Docket No. 0039-7292-2 DIV

IN RE APPLICATION OF: KAORI UMEZAWA, ET AL.

SERIAL NO: 09/358,388

FILED: JULY 21, 1999

FOR: SUBSTRATE HAVING SHALLOW TRENCH ISOLATION AND METHOD OF MANUFACTURING THE SAME

ASSISTANT COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231

SIR:

Transmitted herewith is an amendment in the above-identified application.

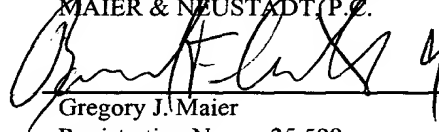
- ☐ No additional fee is required
- ☐ Small entity status of this application under 37 C.F.R. §1.9 and §1.27 has been established by a verified statement previously submitted.
- ☐ Small entity status of this application under 37 C.F.R. §1.9 and §1.27 has been established by a verified statement submitted herewith.
- ☐ Additional documents filed herewith:

The Fee has been calculated as shown below:

CLAIMS	CLAIMS REMAINING		HIGHEST NUMBER PREVIOUSLY PAID	NO. EXTRA CLAIMS	RATE	CALCULATIONS
TOTAL	19	MINUS	20	0	× \$18 =	\$0.00
INDEPENDENT	8	MINUS	3	5	× \$78 =	\$390.00
		<input type="checkbox"/> MULTIPLE DEPENDENT CLAIMS				+ \$260 = \$0.00
		TOTAL OF ABOVE CALCULATIONS				\$390.00
		<input type="checkbox"/> Reduction by 50% for filing by Small Entity				\$0.00
		<input type="checkbox"/> Recordation of Assignment				+ \$40 = \$0.00
		TOTAL				\$390.00

- A check in the amount of **\$390.00** is attached.
- Please charge any additional Fees for the papers being filed herewith and for which no check is enclosed herewith, or credit any overpayment to deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.
- If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. §1.136, and any additional fees required under 37 C.F.R. §1.136 for any necessary extension of time may be charged to Deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.

OBLON, SPIVAK, McCLELLAND,
MAIER & NEUSTADT, P.C.



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0039-7292-2 DIV



IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF: :
KAORI UMEZAWA ET AL. : GROUP ART UNIT: 2814
SERIAL NO: 09/358,388 :
FILED: JULY 21, 1999 : EXAMINER: MAI, A.
FOR: SUBSTRATE HAVING SHALLOW :
TRENCH ISOLATION AND
METHOD OF MANUFACTURING
THE SAME

#7/B
Amdt
J. Manilla
5/26/00
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AMENDMENT

ASSISTANT COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231

SIR:

In response to the outstanding Action mailed February 23, 2000, please amend the
above-identified application as follows:

IN THE CLAIMS

Please cancel Claims 12 and 13 without prejudice or disclaimer.

Please amend Claims 9, 14, and 15 as follows:

Sub C1
BT
--9. (Amended) A method of manufacturing a semiconductor substrate having [a]
shallow trench isolation regions and device regions sandwiched by the shallow trench
isolation regions, comprising the steps of:

(a) a first step of forming a plurality of grooves on part of a surface of the
semiconductor substrate;

(b) a second step of [burying] depositing oxide films in the grooves by an organic